



Robert Greene Sterne
Jorge A. Goldstein
David K.S. Cornwell
Robert W. Esmond
Tracy-Gene G. Durkin
Michele A. Cimbalà
Michael B. Ray
Robert E. Sokohl
Eric K. Steffe
Michael Q. Lee
Steven R. Ludwig
John M. Covert
Linda E. Horner
Robert C. Millonig
Donald J. Featherstone
Timothy J. Shea, Jr.
Michael V. Messinger
Judith U. Kim

Patrick E. Garrett
Jeffrey T. Helvey
Eldora L. Ellison
Thomas C. Flala
Donald R. Banowitz
Peter A. Jackman
Jeffrey S. Weaver
Brian J. Del Buono
Edward W. Yee
Vincent L. Capuano
Virgil Lee Beaston
Theodore A. Wood
Elizabeth J. Haanes
Joseph S. Ostroff
Frank R. Cottingham
Daniel A. Klein
Jason D. Eisenberg
Michael D. Specht

Tracy L. Muller
Jon E. Wright
LuAnne M. DeSantis
Ann E. Summerfield
Helene C. Carlson
Cynthia M. Bouchez
Timothy A. Doyle
Gabby L. Longworth
Lori A. Gordon
Laura A. Vogel
Bryan S. Wade
Bashir M.S. Ali
Shannon A. Carroll
Matthew E. Kelley
Anbar F. Khal
Michelle K. Holoubek
Marsha A. Rose
Young Tang

Christopher J. Walsh
W. Blake Coblenz
James J. Pohl
John T. Haran
Mark W. Rygiel
Kevin W. McCabe

Registered Patent Agents*
Karen R. Markowicz
Matthew J. Dowd
Katrina Yujian Pei Quach
Bryan L. Skelton
Robert A. Schwartzman
Victoria S. Rutherford
Simon J. Elliott
Julie A. Heider
Mita Mukherjee

Scott M. Woodhouse
Liliana Di Nola-Baron
Peter A. Socarras
Jeffrey K. Mills
Danielle L. Letting
Lori Brandes
Steven C. Oppenheimer

Of Counsel
Edward J. Kessler
Kenneth C. Bass III
Marvin C. Guthrie

*Admitted only in Maryland
*Admitted only in Virginia
*Practice Limited to
Federal Agencies

September 8, 2006

WRITER'S DIRECT NUMBER:
(202) 772-8629
INTERNET ADDRESS:
DONF@SKGF.COM

Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

Art Unit 2819

Mail Stop: Amendment

Re: U.S. Utility Patent Application
Appl. No. 10/697,889; Filed: October 31, 2003
For: **Method for Manufacturing a Memory Cell for Modification of
Revision Identifier in an Integrated Circuit Chip**
Inventors: CATALASAN *et al.*
Our Ref: 1875.4360003

Sir:

Transmitted herewith for appropriate action are the following documents:

1. Fourth Supplemental Information Disclosure Statement;
2. List of cited reference on Form PTO/SB/08B (1 page);
3. A copy of reference NPL1 listed on Form PTO/SB/08B (1 document); and
4. One (1) return postcard.

It is respectfully requested that the attached postcard be stamped with the date of filing of these documents, and that it be returned to our courier. In the event that extensions of time are necessary to prevent abandonment of this patent application, then such extensions of time are hereby petitioned.

The U.S. Patent and Trademark Office is hereby authorized to charge any fee deficiency, or credit any overpayment, to our Deposit Account No. 19-0036.

Respectfully submitted,

STERNE, KESSLER, GOLDSTEIN & FOX P.L.L.C.

Donald J. Featherstone
Attorney for Applicants
Registration No. 33,876

DJF/G-A:bac
Enclosures

580394_1.DOC



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

CATALASAN *et al.*

Appl. No.: 10/697,889

Filed: October 31, 2003

For: **Method for Manufacturing a
Memory Cell for Modification of
Revision Identifier in an Integrated
Circuit Chip**

Confirmation No.: 9019

Art Unit: 2819

Examiner: Tan, Vibol

Atty. Docket: 1875.4360003

Fourth Supplemental Information Disclosure Statement

Mail Stop Amendment

Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

Sir:

Listed on accompanying IDS Form PTO/SB/08B is a document that may be considered material to the examination of this application, in compliance with the duty of disclosure requirements of 37 C.F.R. §§ 1.56, 1.97 and 1.98. A copy of the document is enclosed.

Applicants have listed publication dates on the attached IDS Form PTO/SB/08B based on information presently available to the undersigned. However, the listed publication dates should not be construed as an admission that the information was actually published on the date indicated.

Applicants reserve the right to establish the patentability of the claimed invention over any of the information provided herewith, and/or to prove that this information may not be prior art, and/or to prove that this information may not be enabling for the teachings purportedly offered.

This statement should not be construed as a representation that a search has been made, or that information more material to the examination of the present patent application does not exist. The Examiner is specifically requested not to rely solely on the material submitted herewith.


Filing under 37 C.F.R. § 1.97(b). This Information Disclosure Statement is being filed before the mailing date of a first Office Action on the merits. No statement or fee is required.

It is respectfully requested that the Examiner initial and return a copy of the enclosed IDS Form PTO/SB/08B, and indicate in the official file wrapper of this patent application that the documents have been considered.

The U.S. Patent and Trademark Office is hereby authorized to charge any fee deficiency, or credit any overpayment, to our Deposit Account No. 19-0036.

Respectfully submitted,

STERNE, KESSLER, GOLDSTEIN & FOX P.L.L.C.



Donald J. Featherstone
Attorney for Applicants
Registration No. 33,876

Date: September 8, 2006

1100 New York Avenue, N.W.
Washington, D.C. 20005-3934
(202) 371-2600

573336_1.DOC

Under the Paperwork Reduction Act of 1995, no person is required to respond to a collection of information unless it contains a valid OMB control number.

Substitute for form 1449/PTO		Complete if Known	
FOURTH SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Use as many sheets as necessary)</i>		Application Number	10/697,889
		Filing Date	October 31, 2003
		First Named Inventor	Manolito M. CATALASAN
		Art Unit	2819
		Examiner Name	Tan, Vibol
Sheet	1	of	1
		Attorney Docket Number	1875.4360003

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume issue number(s), publisher, city and/or country where published	T ²
	NPL1	Office Action, mailed August 10, 2006, in U.S. Patent Application No. 10/697,079, filed October 31, 2003, 6 pages.	
	NPL2		
	NPL3		
	NPL4		
	NPL5		
	NPL6		
	NPL7		
	NPL8		
	NPL9		
	NPL10		

573330_1.DOC

Examiner Signature		Date Considered	
-----------------------	--	--------------------	--

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Applicant's unique citation designation number (optional). ² Applicant is to place a check mark here if English language Translation is attached.

This collection of information is required by 37 CFR 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

If you need assistance in completing the form, call 1-800-PTO-9199 (1-800-786-9199) and select option 2.